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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10035116	FILING DATE 03/01/2002	CLASS 438	SUBCLASS 3:3	GAU 2812	EXAMINER _____
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****APPLICANTS:** Mori Toru; Yamazaki Takao; Nakase Koichiro;
28

****CONTINUING DATA VERIFIED:**

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**** FOREIGN APPLICATIONS VERIFIED:**
JAPAN 2001-056950 01/03/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials _____		ATTORNEY DOCKET NO PF-2944/ NEC/US/mh

TITLE : Stacked capacitor and method of forming the same as well as semiconductor device using the same and circuit board using the same

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
			Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
PREPARED FOR ISSUE			
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